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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10015913	01/09/2002	438		2812	gebremariam

**APPLICANTS: Singh Rana; Ingersoll Paul;

**CONTINUING DATA VERIFIED:
THIS APPLICATION IS A CIP OF 09/997,145 11/29/2001

T THOMAS

** FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	ATTORNEY DOCKET NO
Foreign priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no		
35 USC 119 conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no		
Verified and Acknowledged Examiners's initials	<i>Singh Rana</i>		SC11448TP P01

TITLE : Semiconductor device structure and method for forming

U.S. DEPT. OF COMM./PAT & TM-PTO-436L(Rev. 12-94)

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NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G
Assistant Examiner		DRAWING	
Amount Due		Sheets Drwg.	Figs. Drwg.
Date Paid		Print Fig.	
Primary Examiner		Applicati n Examiner	
PREPARED FOR ISSUE			
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